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June 5, 2001

TO (FIRM): Patent and Trademark Office

ATTN: Dr. Hugh Jones, Examiner

FAX NO.: (703) 308-2840

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FROM: Christine Joan Gilsdorf

RE: Letter to Examiner Providing Proposed Claim Amendments for Examiner Interview

YOUR REF.: 09/045,041

OUR DOCKET: 122.1329

NO. OF PAGES (Including this Cover Sheet) 4

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COMMENTS:



Docket No. 122.1329/CJG

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hisanori FUJISAWA

Serial No.: 09/045,041

Group Art Unit: 2763

Filed: March 20, 1998

Examiner: H. Jones

For: METHOD AND APPARATUS FOR CARRYING OUT CIRCUIT SIMULATION

LETTER TO THE EXAMINER PROVIDING PROPOSED
CLAIM AMENDMENTS FOR EXAMINER INTERVIEWAssistant Commissioner of Patents
Washington, D.C. 20231

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Sir:

Attached please find draft claims for purposes of discussion at the Examiner Interview scheduled for June 7, 2001.

Your consideration of the draft claims is respectfully requested.

Respectfully submitted,

STAAS & HALSEY

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By:

C. Joan Gilsdorf
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on June 5th 2001

STAAS & HALSEY

By: Regina Knight BarkerDate: June 5, 2001

Serial No. 09/045,041

Docket No. 122.1329CJG

ATTACHMENT A

9. (TWICE AMENDED) A computer-readable storage controlling a computer to simulate a circuit, and comprising a process of: [A method of carrying out simulation of a circuit, comprising:]

inputting data comprising configurations for a plurality of partial circuits, connectional relationships for input and output terminals of the partial circuits, and operational characteristics of components of the partial circuits;

extracting, from the circuit to be simulated, the plurality of partial circuits to inspect for equivalent operational characteristics;

inspecting the plurality of partial circuits to detect partial circuits exhibiting equivalent operational characteristics, based on the configurations of the plurality of partial circuits; and

compressing the circuit by integrating the partial circuits exhibiting equivalent operational characteristics into one circuit and simulating the compressed circuit.

Serial No. 09/045,041

Docket No. 122.1329CJG

ATTACHMENT B

46. (NEW) A method of simulating an LSI circuit, comprising:

- inputting data comprising configurations for a plurality of partial circuits, connectional relationships of components and input and output terminals of the partial circuits, and operational characteristics of components of the partial circuits;
- extracting a plurality of partial circuits from the LSI circuit based upon similarities of corresponding circuit components and input/output terminal connections of the partial circuits;
- comparing the extracted partial circuits for equivalent configurations based upon the types of circuit components and their connectional relationships;
- comparing corresponding circuit components of the partial circuits having equivalent configurations for equivalent operational characteristics;
- comparing the input/output terminals of the partial circuits having circuit components with equivalent operational characteristics to locate partial circuits having identical input/output terminals, the partial circuits having identical input/output terminals being considered as partial circuits having equivalent operational characteristics; and
- compressing the LSI circuit by integrating the partial circuits having equivalent operational characteristics into one circuit, and simulating the compressed circuit.

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need names & phone numbers!*

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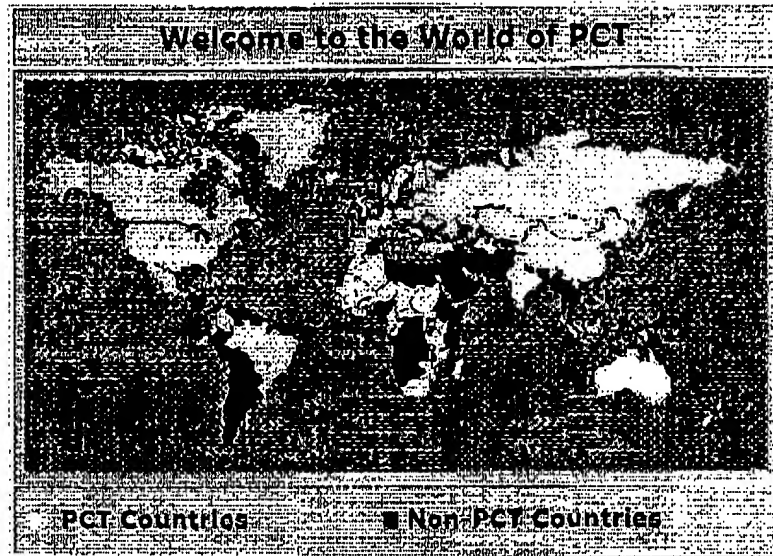
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local point in the United Nations system for technical cooperation with developing countries in trade promotion
- **ITA (International Trade Administration - US)**
- **USTR (United States Trade Representatives Office - EOPresident)** - investigate unfair practices - Special 301
- **Trilateral Website EPO - IPO - USPTO**
- **World Intellectual Property Organization (WIPO)**
- **World Customs Organization**
- **World Trade Organization (WTO)**
- **other related non-USPTO websites**

Regional Organizations

- **UNECA (UN Economic Commission for Africa)**
- **APEC (Asian-Pacific Economic Cooperation)**
- **ESCAP (UN Economic and Social Committee for Asia and the Pacific)**
- **UNECE (UN Economic Commission for Europe)**
- **ECLAC (UN Economic Commission for Latin America & the Caribbean)**
- **ESCWA (UN Economic and Social Commission for Western Asia)**

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- **UNCITRAL (United Nations Commission on International Trade Law)**
- **European Union (EU) - laws & policies**

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- **international IP topics**
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